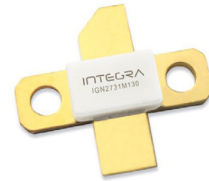


# S-Band, GaN/SiC, RF Power Transistor

2.7 - 3.1GHz | 130 W typ | 55% Efficiency typ | 14.8 dB Gain typ | 50 V | 100µs Pulse Length, 10% Duty Cycle

IGN2731M130 is a high power GaN-on-SiC RF power transistor that has been designed to suit the unique needs of modern radar systems. It supplies a minimum of 130W of peak output power, with typically >14.8 dB of gain and 55% efficiency. It operates from a 50 V supply voltage. For optimal thermal efficiency, the transistor is housed in a metal-based package with an epoxy-sealed ceramic lid.

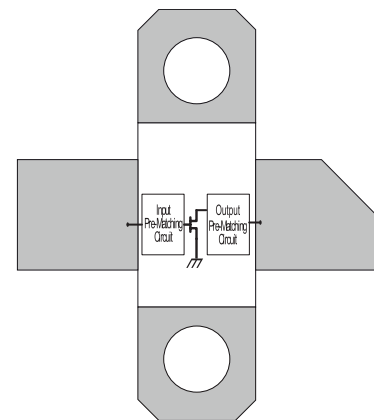


## FEATURES

- GaN on SiC HEMT Technology
- Output Power >130W
- Pre-matched Input and Output Impedances
- High Efficiency - 55% typical
- 10:1 VSWR Withstand
- 100% RF Tested Under 100µs, 10% duty cycle pulse conditions
- RoHS and REACH Compliant

## APPLICATIONS

- S-band Radar Systems



**Table 1. Absolute Maximum Ratings (Not Simultaneous)**

Parameter	Symbol	Value	Units	Test Conditions
DC Drain-Source Voltage	$V_{DS}$	160	V	25 °C
DC Gate-Source Voltage	$V_{GS}$	-8 to +1.5	V	25 °C
DC Drain Current	$I_D$	12	A	25 °C
DC Gate Current	$I_G$	2.4	mA	25 °C
RF Input Power	$P_{RF,IN}$	4.5W	W	25 °C
Operating Junction Temperature	$T_J$	-55 to +200	°C	
Storage Temperature	$T_{STG}$	-55 to +150	°C	
Soldering Temperature	$T_{SOLDER}$	260 for 60s	°C	

Note: Operation outside the limits given in this table may cause permanent damage to the transistor

**Table 2. DC Electrical Characteristics (Case temperature = 25 °C unless otherwise stated)**

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Gate Pinch-Off Voltage	$V_P$	-5.0			V	$V_{DS} = 50V, I_{DS} = 4mA$
Quiescent Gate Voltage	$V_Q$		-2.6		V	$V_{DS} = 50V, I_{DS} = 25mA$

**Table 3. RF Electrical Characteristics (Case temperature = 30 °C unless otherwise stated)**

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Gain	G	14.1	14.8	17.1	dB	$P_{IN} = 4.5W$ $f = 2.7, 2.9, 3.1 \text{ GHz}$ $100\mu s$ pulse length, 10% duty cycle $V_{DS} = 50V, I_{DS} = 25mA$ 100% RF Tested
Drain Efficiency	$\eta$	50	55	75	%	
Pulse Droop	D	-0.5	-0.2	+0.3	dB	
Input Return Loss	IRL	-18	-12	-7	dB	
Load Mismatch Stability	VSWR-S	2:1				
VSWR Withstand	VSWR-LMT	3:1				

Note: Consult Integra Technologies Application Note 001 for information on how RF output power and pulse droop are measured.

**Table 4. VSWR Withstand Capability (Case temperature = 25 °C unless otherwise stated)**

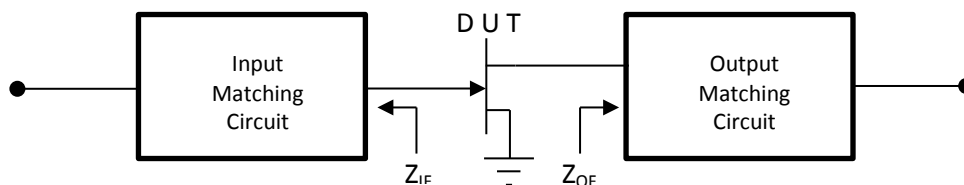
Parameter	Symbol	Typ	Units	Test Conditions
VSWR Withstand Capability	VSWR	10:1	Ratio	$P_{OUT} = 130W$ $f = 2.9 \text{ GHz}$ $100\mu s$ pulse length, 10% duty cycle $V_{DS} = 50V, I_{DS} = 25mA$

**Table 5. Thermal Resistance (Case temperature = 25 °C unless otherwise stated)**

Parameter	Symbol	Typ	Units	Test Conditions
Peak Thermal Resistance, Channel to Case	$R_{TH}$	0.5	°C/W	$P_{OUT} = 130W$ $f = 2.9 \text{ GHz}$ $100\mu s$ pulse length, 10% duty cycle $V_{DS} = 50V, I_{DS} = 25mA$

**Table 6. Optimum Source & Load Impedances (Case temperature = 25 °C unless otherwise stated)**

Frequency (GHz)	$Z_{IF}$	$Z_{OF}$	Units	Test Conditions
2.7	4.9 - j 8.8	8.0 - j 7.8	$\Omega$	$P_{OUT} = 130W$ $100\mu s$ pulse length, 10% duty cycle $V_{DS} = 50V, I_{DS} = 25mA$
2.9	4.9 - j 7.1	7.6 - j 6.6	$\Omega$	
3.1	5.2 - j 6.5	7.3 - j 6.0	$\Omega$	



**TYPICAL PERFORMANCE**

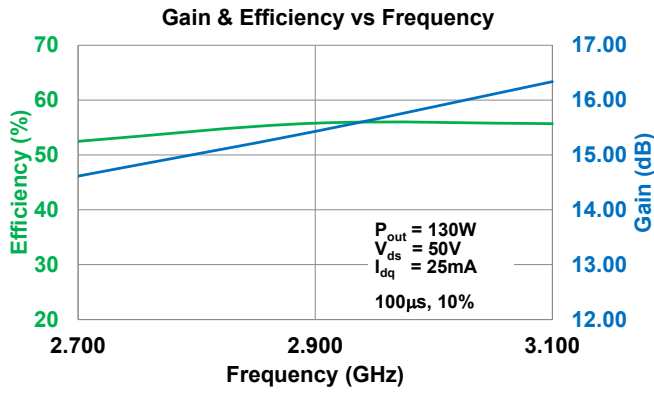


Figure 1

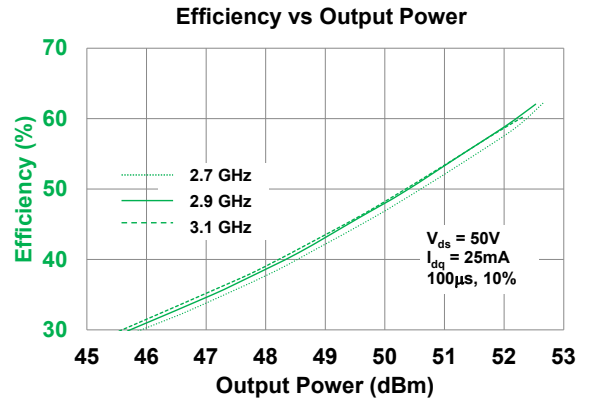
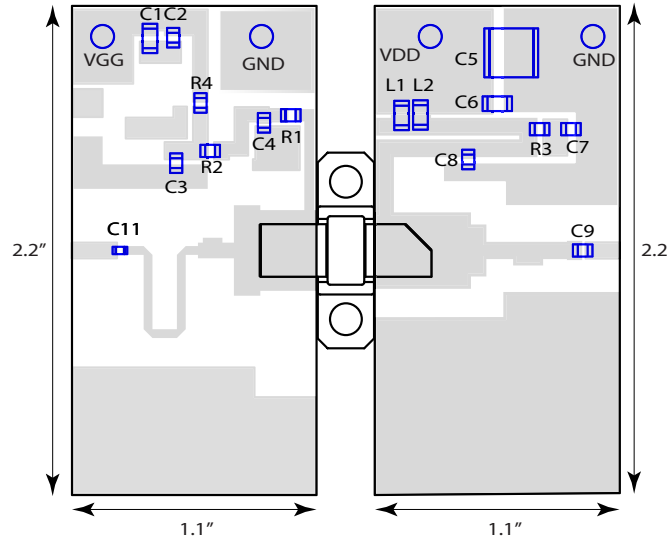


Figure 2

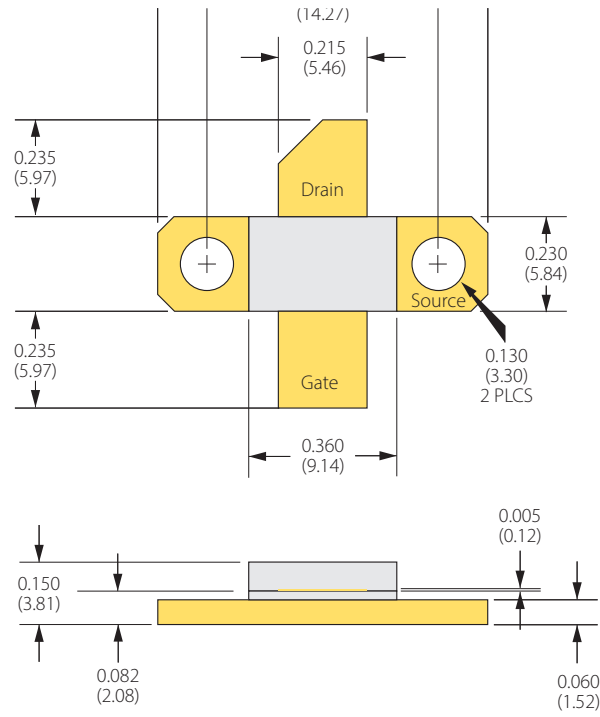
**TEST FIXTURE**



**Bill of Materials for IGN2731M130 Test Fixture**

Designator	Description	Quantity	Part Number
C1, C6	CAP 1 $\mu$ F, 1206, 100V, X7R	2	GRM31CR72A105KA01L
C2, C3, C7	CAP 0.1 $\mu$ F, 0805, 100V	3	C0805C104K1RAL
C4	CAP 5.6PF, 0805, 250V	1	ATC600F5R6CT
C5	CAP 10 $\mu$ F, 2220, 50V, X7R,20%	1	C575X7R1H106M
C8, C9	CAP 12pF, 0805, 250V	2	ATC600F120CT
C10	CAP 68 $\mu$ F, 63V, Electrolytic	1	140-XRL63V68
C11	CAP, 10pF, 0603, 250V	1	ATC600S100JT
C12	CAP 1.2pF, 0805, 250V	1	ATC600F1R2BT
L1, L2	IND, FB, 1206, 6A, 33 OHM-100MHz	2	BLM31PG330S
R1, R3	RES, 10 OHM, 0805	2	
R2	RES, 10 OHM, 0805	1	
L1, L2	IND FB, 33 OHM, 1206, 6A	2	BLM31PG330S
PC Board Type	ROGERS 30 mil, 1oz/1oz, Copper	2	RO4350B-03011

**PACKAGE PL32A1**



**Dimensions: Inches (mm)**

### ESD & MSL Rating

Parameter	Rating	Standard
ESD Human Body Model (HBM)	TBD	ESDA/JEDEC JS-001-2012
ESD Charged Device Model (CDM)	TBD	JEDEC JESD22-C101F
Moisture Sensitivity Level (MSL)	Unlimited Shelf Life	IPC/JEDEC J-STD-020

### RoHS Compliance

Integra Technologies, Inc declares that its GaN and LDMOS Transistor Products comply with EU Directive 2011/65/EU on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863/EU.

### REACH Compliance

Integra Technologies supports EU Regulation number 1907/2006 concerning the Registration, Evaluation, Authorization, and Restriction of Chemicals (REACH) as these apply to Integra semiconductor products, development tools, and shipping packaging.

In support of the REACH regulation, Integra will:

- Inform customers and recipients of Integra product if they contain any substances that are of very high concern (SVHC) per the European Chemical Agency (ECHA) website.
- Notify ECHA if any Integra product that contains any SVHCs which exceed guidelines for REACH chemicals by weight per part number and for total content weight per year for all products produced in or imported to the European market.
- Cease shipments of product containing REACH Annex XIV substances until authorization has been obtained.
- Cease shipment of product containing REACH Annex XVII chemicals when restrictions apply.

Integra has evaluated its materials, BOMs, and product specifications and product and has determined that this transistor conforms to all REACH and SVHC regulations and guidelines. Integra has implemented actions and control programs that will assure continued compliance.

### Disclaimer

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#### DEFINITIONS:

##### DATA SHEET STATUS

Advanced Specification - This data sheet contains Advanced specifications.

Preliminary Specification - This data sheet contains specifications based on preliminary measurements and data.

Final Specification - This data sheet contains final product specifications.

**MAXIMUM RATINGS** Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.

Integra Technologies, 321 Coral Circle, El Segundo, CA 90245-4620 | Phone: 310-606-0855 | Fax: 310-606-0865