

S-Band, GaN/SiC, RF Power Transistor

2.856 GHz | 40 W | 60% Efficiency typ | 17.8dB Gain typ | 50 V | 12 μ s Pulse Length, 3% Duty Cycle

IGN2856S40 and IGN2856S40S are high power GaN-on-SiC RF power transistors that have been designed to suit the unique needs of ISM systems. They supply a minimum of 40W of peak output power, with typically 17.8dB of gain and 60% efficiency. They operate from a 50 V supply voltage. For optimal thermal efficiency, the transistors are housed in a metal-based package with an epoxy-sealed ceramic lid.



FEATURES

- GaN on SiC HEMT Technology
- Output Power 40W
- Pre-matched Input and Output Impedances
- High Efficiency - 60% typical
- 100% RF Tested under 12 μ s, 3% duty cycle pulse conditions
- RoHS and REACH Compliant

APPLICATIONS

- ISM

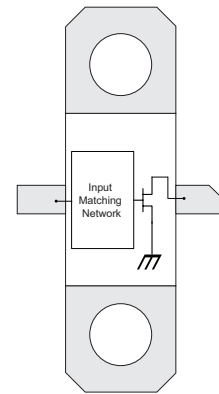


Table 1. Absolute Maximum Ratings (Not Simultaneous)

Parameter	Symbol	Value	Units	Test Conditions
DC Drain-Source Voltage	V_{DS}	140	V	25 °C
DC Gate-Source Voltage	V_{GS}	-8 to +1	V	25 °C
DC Drain Current	I_D	3.6	A	25 °C
DC Gate Current	I_G	3.6	mA	25 °C
RF Input Power	P_{RFIN}	1	W	25 °C
Operating Channel Temperature	T_J	-55 to +225	°C	
Storage Temperature	T_{STG}	-55 to +150	°C	
Soldering Temperature	T_{SOLDER}	260 for 10s	°C	

Note: Operation outside the limits given in this table may cause permanent damage to the transistor

Table 2. DC Electrical Characteristics (Case temperature = 25 °C unless otherwise stated)

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Gate Pinch-Off Voltage	V_P	-5.0			V	$V_{DS} = 50V, I_{DS} = 1mA$
Quiescent Gate Voltage	V_Q		-2.8		V	$V_{DS} = 50V, I_{DS} = 15mA$

Table 3. RF Electrical Characteristics (Case temperature = 30 °C unless otherwise stated)

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
RF Input Power	$P_{OUT,RF}$	40	60	80	W	$P_{IN} = 1W$ $f = 2.856 \text{ GHz}$ 12 μ s pulse length, 3% duty cycle $V_{DS} = 50V, I_{DS} = 15mA$
Gain	G	16	17.8	19	dB	
Drain Efficiency	η	55	60	70	%	
Pulse Droop	D	-0.5	-0.2	+0.3	dB	
Input Return Loss	IRL	10	15	18	dB	
VSWR Mismatch Stability	VSWR-S	2:1				
VSWR Withstand	VSWR-LMT	3:1				

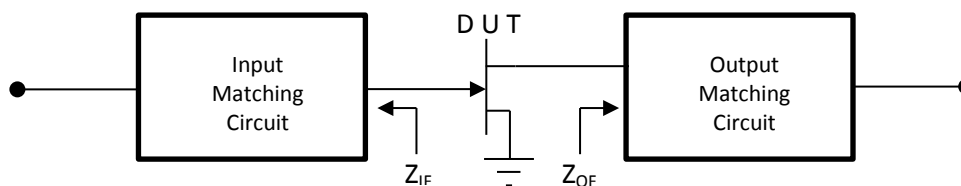
Note: Consult Integra Technologies Application Note 001 for information on how RF output power and pulse droop are measured.

Table 4. Thermal Resistance (Case temperature = 85 °C unless otherwise stated)

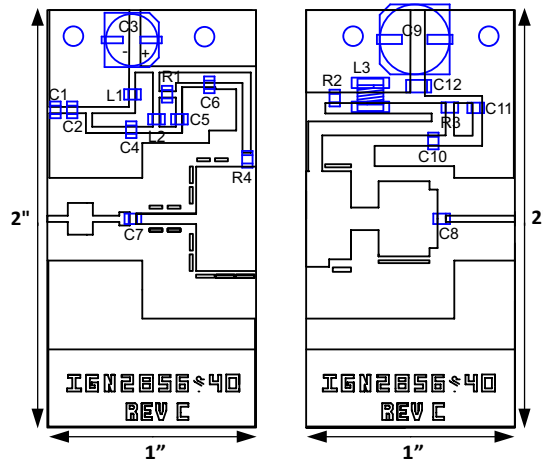
Parameter	Symbol	Typ	Units	Test Conditions
Peak Thermal Resistance, Channel to Case	R_{TH}	1.8	°C/W	Dissipated Power = 32W 12 μ s pulse length, 3% duty cycle $V_{DS} = 50V, I_{DS} = 15mA$

Table 5. Matching Circuit Impedances (Case temperature = 25 °C unless otherwise stated)

Frequency (GHz)	Z_{IF}	Z_{OF}	Units	Test Conditions
2.856	17.0 - j 15.5	9.0 - j 4.4	Ω	$P_{OUT} = 40W$ 12 μ s Pulse length, 3% Duty Cycle $V_{DS} = 50V, I_{DS} = 15mA$



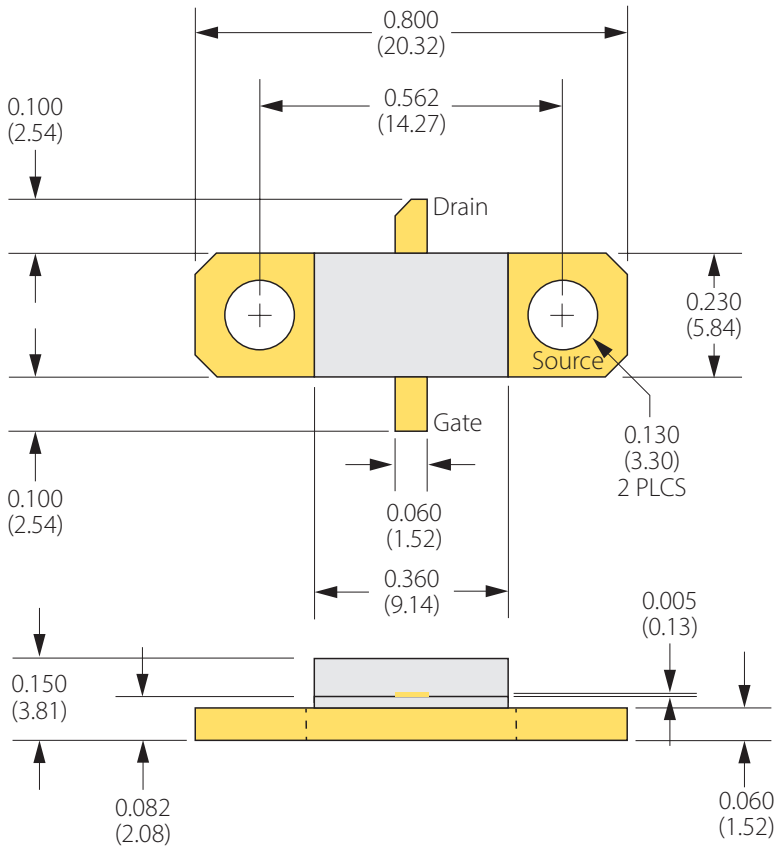
TEST FIXTURE



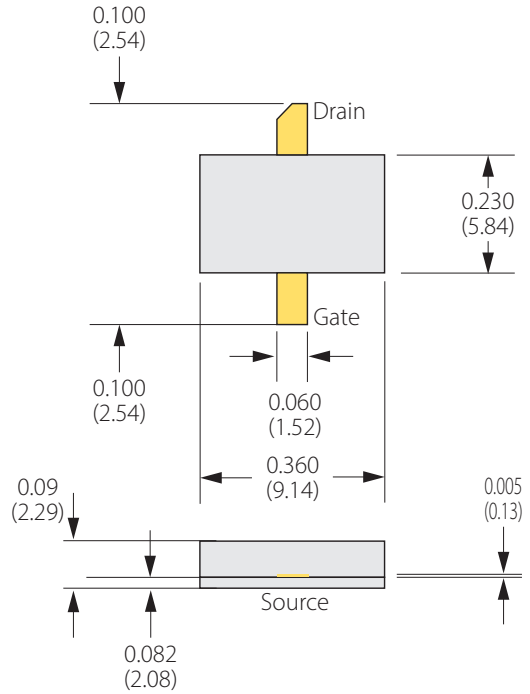
Bill of Materials for IGN2856S40 Test Fixture

Designator	Description	Quantity
C1, C4, C11	CAP 0.1 μ F, 0805, 100V	3
C2, C5, C6	CAP 27pF, 0805, 50V	3
C3	CAP 47 μ F, 25V	1
C7, C8	CAP 18pF, Edge Mount	2
C9	CAP 68 μ F, 63V, Edge Mount	1
C10	CAP 5.6pF, Edge Mount	1
C12	CAP 1 μ F, 1206, 100V	1
L1, L2	IND FB, 120 OHM, 0805, 5A	2
L3	IND 5.5nH, 1508	1
R1, R2, R2, R4	RES 5.1 OHM, 0805	4
PC Board Type	ROGERS 4350B-03011, 30mils, 1/1 oz Cu	2

PACKAGE PL32A2



**BOLT-DOWN FLANGE OPTION
IGN2856S40**



**EARLESS FLANGE OPTION
IGN2856S40S**

Dimensions: Inches (mm)

ESD Rating

Parameter	Rating	Standard
ESD Human Body Model (HBM)	TBD	ESDA/JEDEC JS-001-2012
ESD Charged Device Model (CDM)	TBD	JEDEC JESD22-C101F
Moisture Sensitivity Level (MSL)	Unlimited Shelf Life	IPC/JEDEC J-STD-020

RoHS Compliance

Integra Technologies, Inc declares that its GaN and LDMOS Transistor Products comply with EU Directive 2011/65/EU on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863/EU.

REACH Compliance

Integra Technologies supports EU Regulation number 1907/2006 concerning the Registration, Evaluation, Authorization, and Restriction of Chemicals (REACH) as these apply to Integra semiconductor products, development tools, and shipping packaging.

In support of the REACH regulation, Integra will:

- Inform customers and recipients of Integra product if they contain any substances that are of very high concern (SVHC) per the European Chemical Agency (ECHA) website.
- Notify ECHA if any Integra product that contains any SVHCs which exceed guidelines for REACH chemicals by weight per part number and for total content weight per year for all products produced in or imported to the European market.
- Cease shipments of product containing REACH Annex XIV substances until authorization has been obtained.
- Cease shipment of product containing REACH Annex XVII chemicals when restrictions apply.

Integra has evaluated its materials, BOMs, and product specifications and product and has determined that this transistor conforms to all REACH and SVHC regulations and guidelines. Integra has implemented actions and control programs that will assure continued compliance.

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DEFINITIONS:

DATA SHEET STATUS

Advanced Specification - This data sheet contains Advanced specifications.

Preliminary Specification - This data sheet contains specifications based on preliminary measurements and data.

Final Specification - This data sheet contains final product specifications.

MAXIMUM RATINGS Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.

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