

## S-Band Radar Transistor

The high power pulsed radar transistor device part number IB2226MH110 is designed for S-Band radar systems operating over the instantaneous bandwidth of 2.25-2.55 GHz. While operating in class C mode this common base device supplies a minimum of 110 watts of peak pulse power under the conditions of 200 $\mu$ s pulse width and 10% duty cycle. All devices are 100% screened for large signal RF parameters, including power gain compression. Excellent spectral stability into output mismatch over a broad input power range make it ideal for use in reliable high power solid state transmitters.



### Silicon Bipolar

- Ultra-high  $f_T$

### Class C Operation

- High Efficiency

### Common Base Configuration

- Single Power Supply

### Gold Metal

- Maximum Reliability

### Emitter Ballasting

- Optimum Thermal Distribution

### Internal Impedance Matching

- Ease of Use
- Ultra-low Loss Design

### Be0 Package

- Solder Seal Hermeticity
- Unmatched Thermal Reliability

### RF Test Fixture

- Broadband
- Matched to 50 $\Omega$
- Long-term Correlation
- 100% Device RF Screening
- No External Tuning Allowed
- Micro-strip structure on soft pc board with dielectric constant 10.2

*TYPICAL DATA*    *TYPICAL DATA*    *TYPICAL DATA*    *TYPICAL DATA*

Test Sequence Name	Freq (GHz)	PW (us)	Duty (%)	Vcc (V)	P <sub>IN</sub> (W)	IRL (dB)	P <sub>OUT</sub> (W)	G <sub>P</sub> (dB)	I <sub>C</sub> (A)	N <sub>C</sub> (%)	Drop (dB)	VSWR 1.5:1	VSWR 2:1
+0.75dB	2.250	200	10	36.0	19.02	--	--	--	--	--	--	P	--
Nominal	2.250	200	10	36.0	16.00	-14	144	9.5	9.30	43	-0.46	--	P
+0.75dB	2.400	200	10	36.0	19.02	--	--	--	--	--	--	P	--
Nominal	2.400	200	10	36.0	16.00	-32	133	9.2	8.48	44	-0.31	--	P
+0.75dB	2.550	200	10	36.0	19.02	--	--	--	--	--	--	P	--
Nominal	2.550	200	10	36.0	16.00	-19	124	8.9	7.78	44	-0.27	--	P

**MAXIMUM RATINGS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Collector-Emitter Voltage	$V_{CES}$	--	70	V	$V_{BE}=0V$
BD	Storage Temperature Range	$T_{STG}$	-55	+200	°C	--
BD	Operating Junction Temperature Range	$T_J$	-55	+200	°C	--
Note	Screen 'BD' = parameter qualified By Design.					

**THERMAL CHARACTERISTICS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
BD	Thermal Resistance	$R_{TH(JC)}$	--	0.44	°C/W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{OUT}= 110W.$
Note	Screen 'BD' = parameter qualified By Design.					

**PROCESSING SPECIFICATIONS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	DC Wafer Probe	--	--	--	--	Per Integra specification.
Q1	Wafer DC and RF Qualification	--	--	--	--	Per Integra specification.
LM	Wire Bond Strength	--	--	--	--	Line monitor per Integra specification.
100%	Pre-cap visual inspection	--	--	--	--	Per Integra specification
100%	Gross leak test	--	--	--	--	MIL-STD-750D, Method 1071.6, Test Condition C
Note	Screen 'Q1' = parameter is qualified by assembly and test of 3 pieces minimum per wafer.					
Note	Screen 'LM' = parameter is qualified by assembly line monitor.					

**DC ELECTRICAL CHARACTERISTICS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Collector-Emitter Breakdown Voltage	$BV_{CES}$	65	--	V	$I_C = 30mA, V_{BE} = 0V, T_F = 25\pm5^\circ C.$
100%	Zero Base Voltage Collector Leakage Current	$I_{CES}$	--	6	mA	$V_{CE} = 36V, V_{BE} = 0V, T_F = 25\pm5^\circ C.$
100%	DC Current Gain	$H_{FE}$	10	150	--	$V_{CE} = 5V, I_C = 100mA, T_F = 25\pm5^\circ C.$

**RF ELECTRICAL CHARACTERISTICS**

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Input Return Loss	IRL	-18	-7	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F= T_{F1}, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$
100%	Output Power	$P_O$	110	150	W	$V_{CC}=V1, PW=PW1, DF=DF1, T_F= T_{F1}, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$
100%	Collector Efficiency ( $P_O/I_C/V_{CC}$ )	$N_C$	38	100	%	$V_{CC}=V1, PW=PW1, DF=DF1, T_F= T_{F1}, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$
100%	Pulse Amplitude Droop	D	-0.6	0.6	dB	$V_{CC}=V1, PW=PW1, DF=DF1, T_F= T_{F1}, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$
100%	Stability into 1.5:1 VSWR with +0.75dB overdrive	VSWR-S	--	--	--	Repeat $P_O$ with $P_{IN}$ increased by 0.75dB. Rotate 1.5:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse. All non-harmonically related signals must be at least -50 dBc.
100%	2:1 Load Mismatch Tolerance	LMT	--	--	--	$V_{CC}=V1, PW=PW1, DF=DF1, T_F= T_{F1}, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$ Rotate 2:1 output VSWR through 360° phase. Post test $P_O$ = Pre test $P_O \pm 0.2$ dB.
BD	Pulse Risetime	RT	--	150	ns	$V_{CC}=V1, PW=PW1, DF=DF1, T_F= T_{F1}, P_{IN}=P_{IN1}, P_{IN2}, P_{IN3}, F=F1, F2, F3.$ Measure between 10% and 90% detected power points.
Note	$V1 = 36V \pm 0.2V; PW1 = 200\mu s; DF1 = 10%; P_{IN1} = P_{IN2} = P_{IN3} = 16W; F1 = 2.25 \text{ GHz}, F2 = 2.40 \text{ GHz}, F3 = 2.55 \text{ GHz}, T_{F1} = 25 \pm 5^\circ C.$					
Note	$T_F$ = Device flange temperature.					
Note	Screen 'BD' = parameter qualified By Design.					

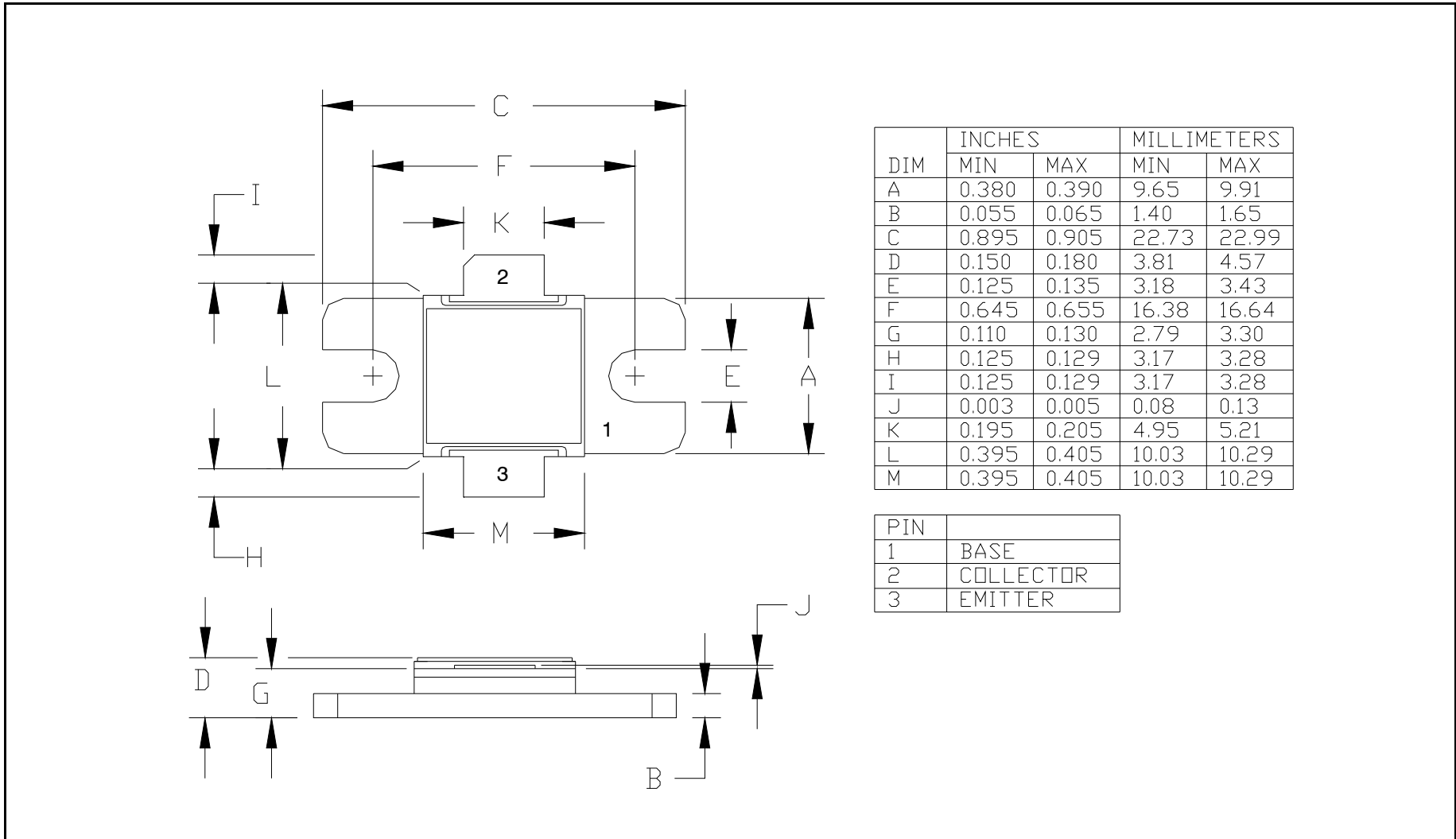
**RF TEST FIXTURE IMPEDANCE CHARACTERISTICS**

Frequency (MHz)	$Z_{IF} (\Omega)$	$Z_{OF} (\Omega)$
2.25	3.3 - j7.7	2.7 - j5.4
2.40	3.0 - j6.9	2.5 - j4.5
2.55	2.8 - j6.1	2.3 - j3.7

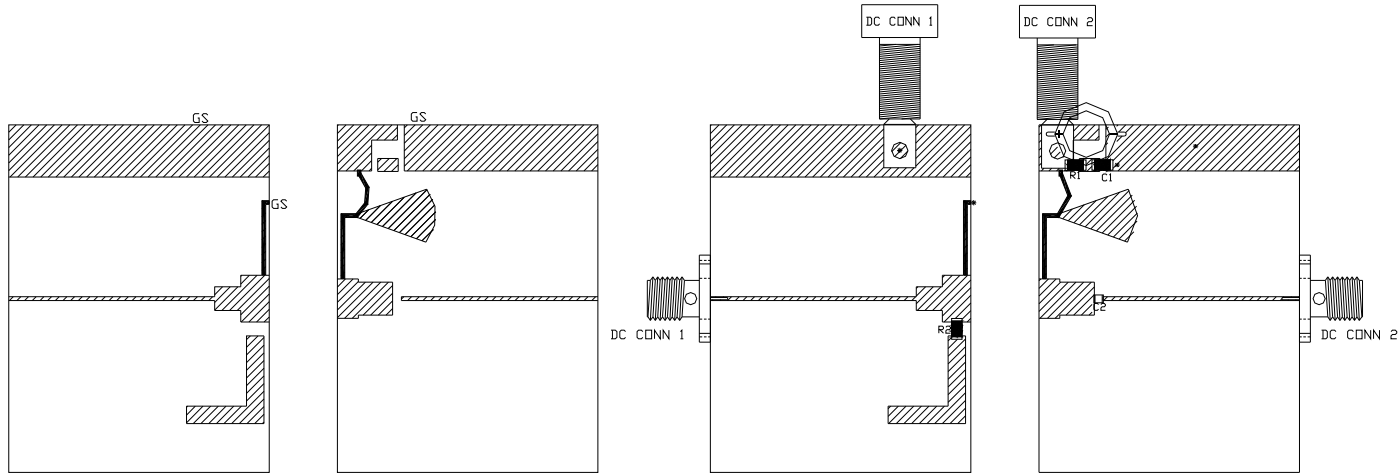
  

Impedance Definition		
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**PACKAGE DIMENSIONAL OUTLINE DRAWING**



**RF TEST FIXTURE**

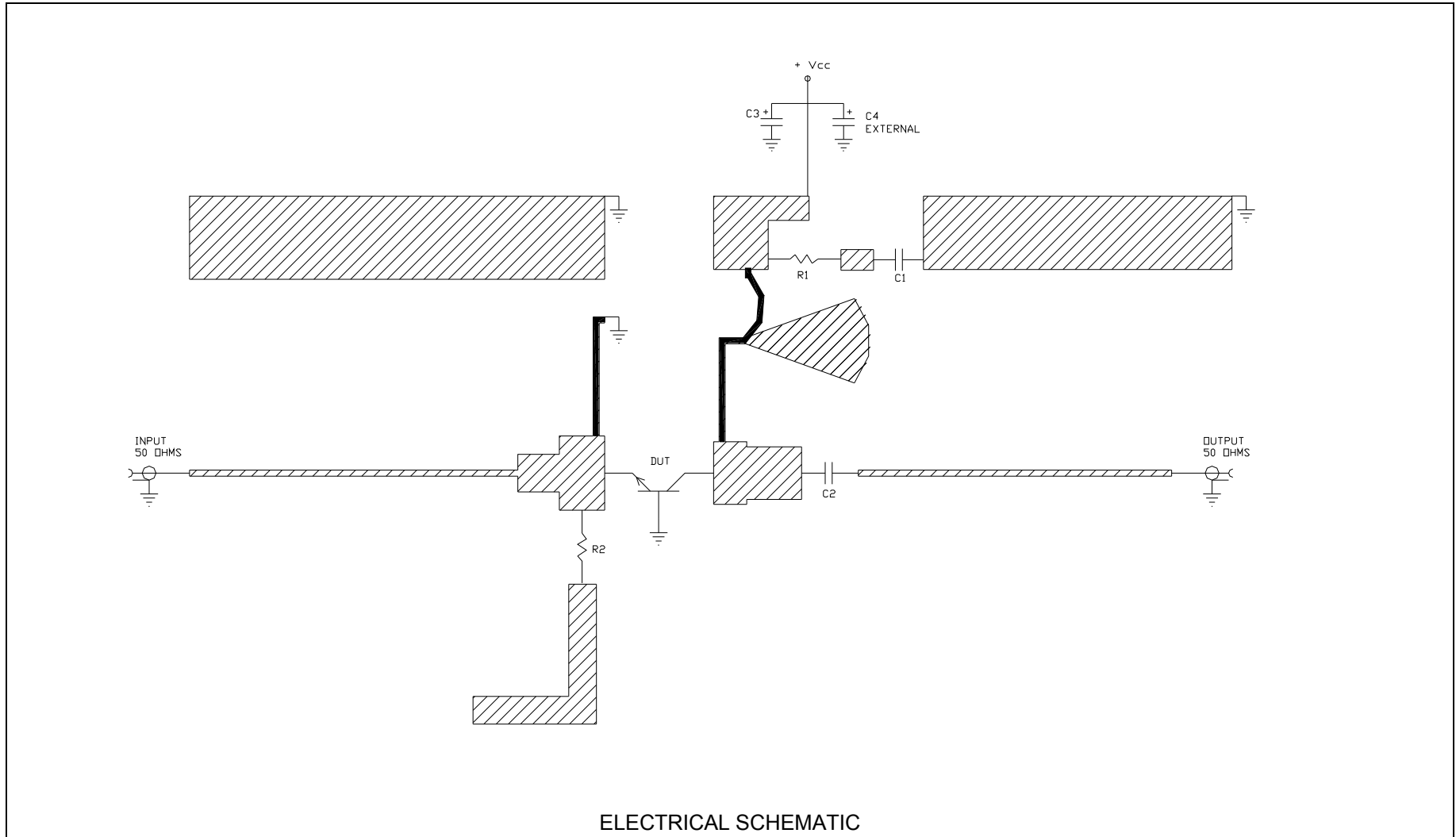


COMPONENT	DESCRIPTION
DUT	TRANSISTOR #IB2226MH110 MOUNT HARD TO THE RIGHT
PC BOARD	ROGERS #RD6010.2 LM .025" 2 Oz Cu
C1	CHIP CAPACITOR .1uF
C2	CHIP CAPACITOR ATC100A - 39pF
C3	CHIP CAPACITOR 68uF/63V
C4 (NOT SHOWN)	ELECTROLYTIC CAPACITOR, 4700uF / 50V
R1	RESISTOR 6.81 OHM
R2	RESISTOR 150 OHM
JUMPER WIRE (1 PLACE)	JUMPER WIRE
BIAS (2 PLACES)	BIAS LINE WIRE
GS (4 PLACES)	GROUND SHIM, COPPER, TH=0.001"
CONN 1, CONN 2	SMA CONNECTOR, DS #2052-5636-02
INPUT PC BOARD CARRIER	2 INCH BRASS-04 (1.5")
OUTPUT PC BOARD CARRIER	2 INCH BRASS-04 (1.5")
TRANSISTOR CARRIER	2 INCH COPPER-02
TRANSISTOR CLAMP	NDRYL CLAMP-02
ALUMINUM HEAT SINK	2 INCH HEATSINK-09
DC CONN 1	BANANA JACK, BLACK
DC CONN 2	BANANA JACK, RED
NOTE	FIXTURE HARDWARE DRAWINGS AVAILABLE ON REQUEST

**ASSEMBLY AND PARTS LIST**



**RF TEST FIXTURE**



**DEFINITIONS**

<b>Data Sheet Status</b>	
Proposed Specification	This data sheet contains proposed specifications.
Preliminary Specification	This data sheet contains specifications based on preliminary measurements and data.
Product Specification	This data sheet contains final product specifications.
<b>Maximum Ratings</b>	
Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only. Operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.	

**WARNING**

<b>Product and environmental safety - toxic materials</b>
This product contains beryllium oxide. The product is entirely safe provided that the BeO base is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with general or domestic waste.

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